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'V		IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, May 2000, Pages 719-723	L
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30 /		WITTERS, Johan S. et al., "Analysis and Modeling of On-Chip High-Voltage Generator Circulistor Use in EEPROM				
		Circuits*, IEEE Journal of Solid-State Circuits, US, IEEE Inc., New York, Vol. 24, No. 5, October 1989, Pages 1372-1380				
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